

FORM PTO-1449 (REV.7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 856063.758	APPLICATION NO.
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)		APPLICANTS Bruno Murari et al.			
		FILING DATE November 26, 2003	GROUP ART UNIT		

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
PN	AA	5,463,347	10/31/95	Jones et al.	330	253	
PN	AB	6,353,345	03/05/02	Yushan et al.	327	112	
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AJ						
	AJ						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
PN	AK	0 657 995	06/14/95	EP		
PN	AL	0 790 703	08/20/97	EP		
PN	AM	101 14 935	10/24/02	DE		X
	AN					
	AO					

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

PN	AP		Hong, Merit Y., "Simulation and Fabrication of Submicron Channel Length DMOS Transistors for Analog Applications," <i>IEEE Transactions of Electron Devices</i> , 40(12):2222-2230, December 1993.
	AQ		
	AR		

EXAMINER	Patricia Nguyen	DATE CONSIDERED
		2/20/05

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).